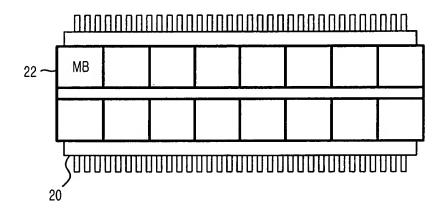
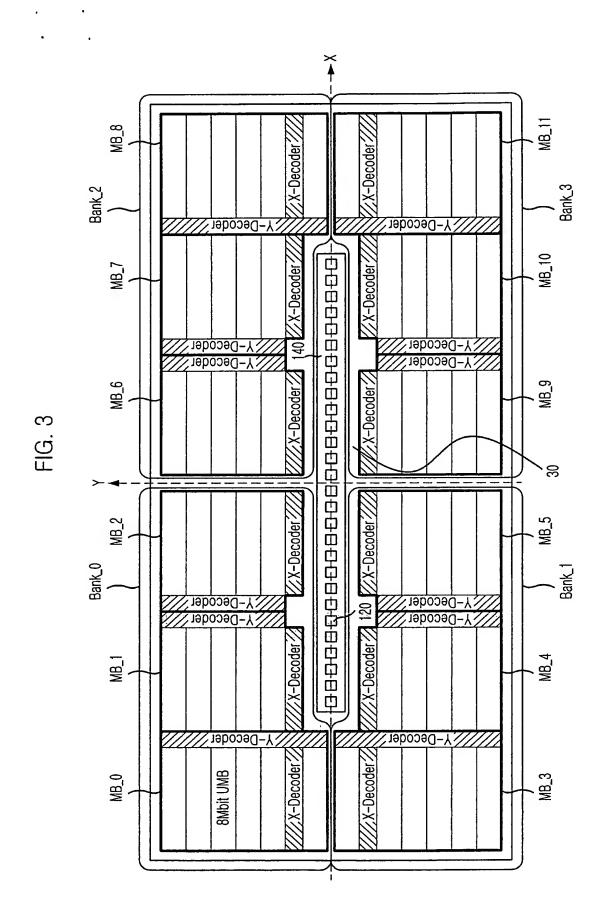
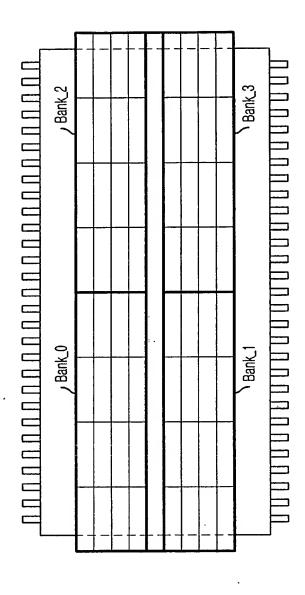


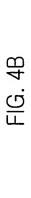
FIG. 2 (PRIOR ART)

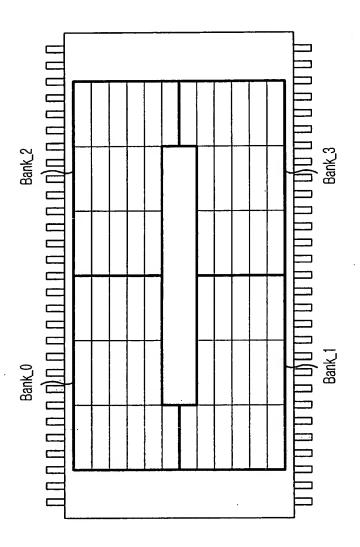




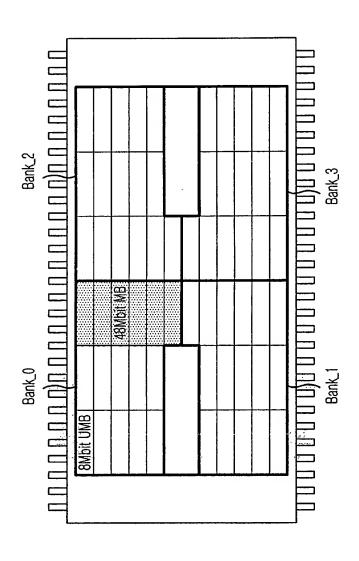


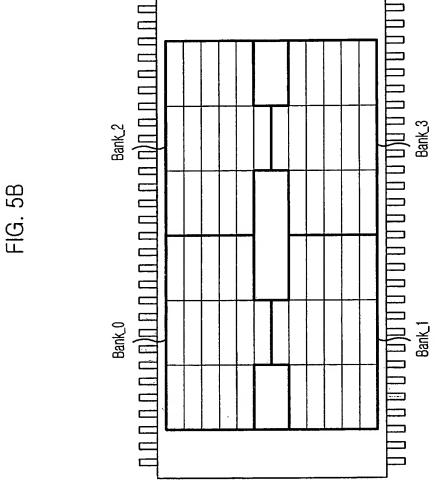






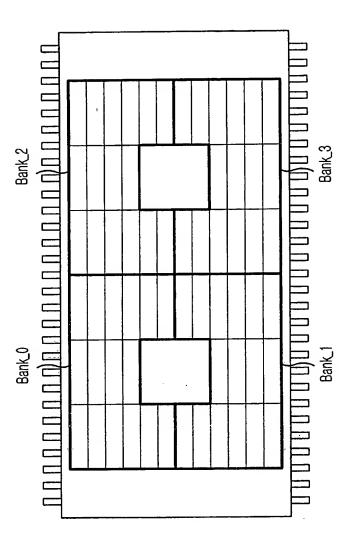




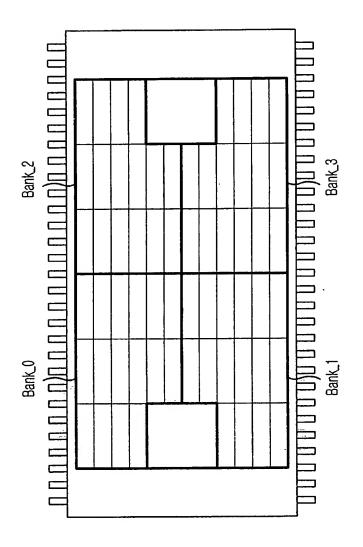


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FIG. 6	Bank_2 \	(1,6)	MB	X-Decoder	(2a,6a) MB		(2b,6b) MB	X-Decoder	(3,6)	MB	
		oder	Y-Dec		Y-Decoder		Y-Decoder		ecoder	U−	
		(1,5)	MB	X-Decoder	[(2,5)			X-Decoder	(3,5)	WB	
		oder	o9O−Y		Y-Decoder		Y-Decoder	Ш	есодег	Q-Y) Bank_3
		(1,4)	WB	X-Decoder	(2a,4a) MB	000000000	(2b,4b) MB	X-Decoder	(3,4)	WB	
								, ,			1
		(1,3)	MB	X-Decoder	(2a,3a) MB	000000000	(2b,3b) MB	X-Decoder	(3,3)	WB WB	
		oder	D9C-Y		Y-Decoder		Y-Decoder		ecoqeı	J-D]
		(1,2)	WB	X-Decoder	(2.2)	0000000		X-Decoder	(3,2)	MB	
		nder)⊃9U-Y		Y-Decoder		Y-Decoder		19booe	Ŋ-γ]
	Bank_0 \	(1,1)	32Mbit MB	X-Decoder	(2a,1a) 16Mbit MB		(2b,1b) MB	X-Decoder	(3,1)	WB	Sank_1
						PAD	<u>!</u>				
						4	•				









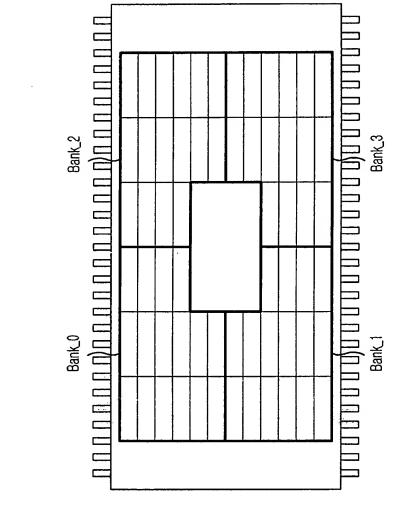
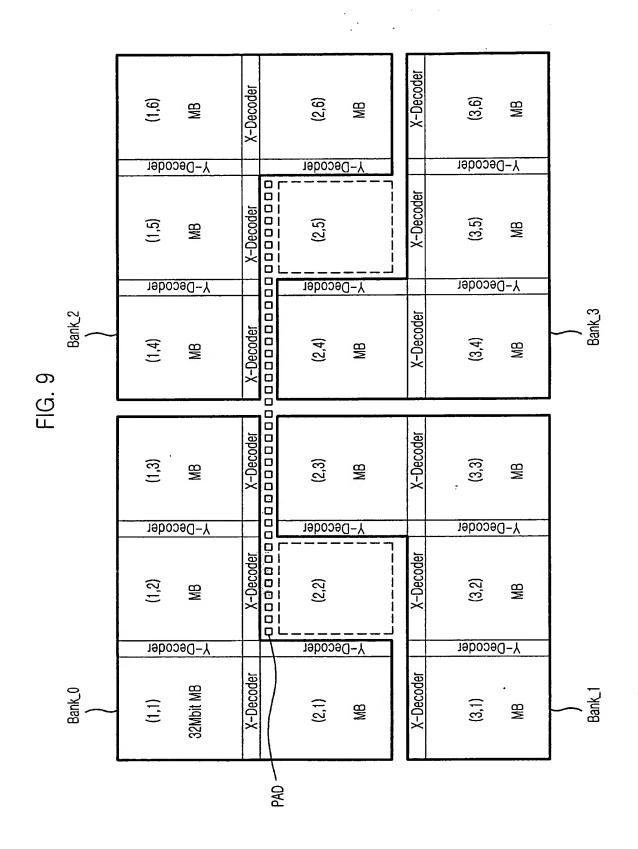
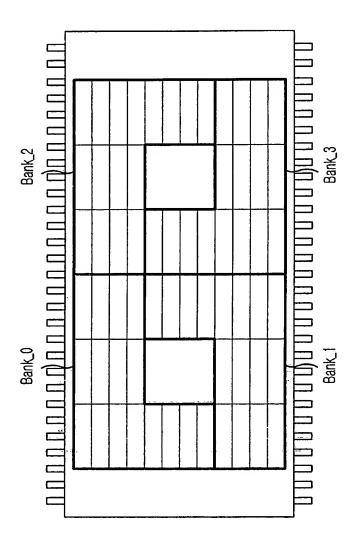
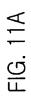


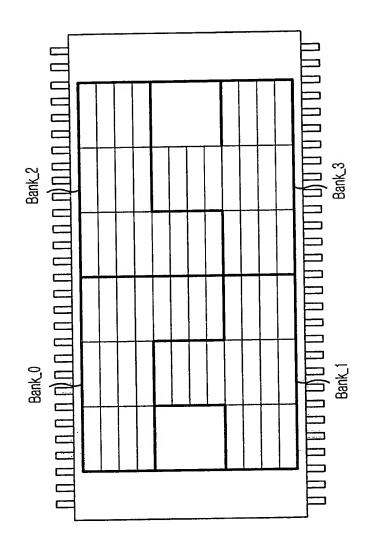
FIG. 8B

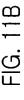












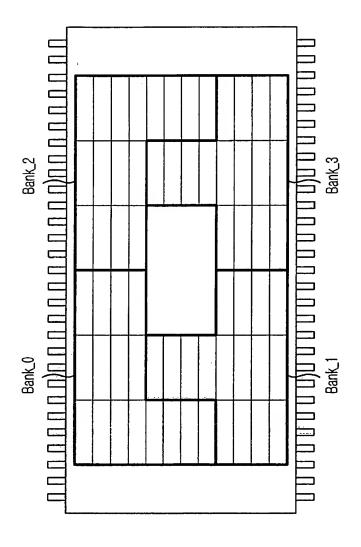


FIG. 12

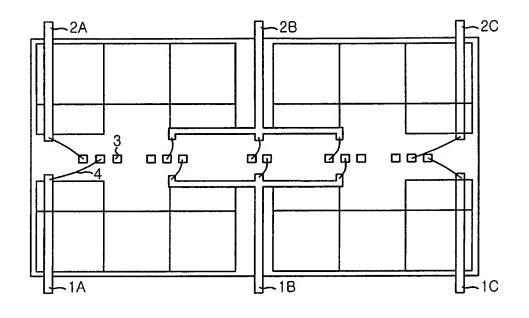


FIG. 13

